

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	TIMING CONTROLLER AND CONTROLLED DELAY CIRCUIT FOR CONTROLLING TIMING OR DELAY TIME OF A SIGNAL BY CHANGING PHASE THEREOF						
<p>Application Number : 10/708,145</p> <p>Confirmation Number:</p> <p>First Named Applicant: Yoshinori OKAJIMA</p> <p>Attorney Docket Number: 960637D</p> <p>Art Unit: 2816</p> <p>Examiner: COX</p> <p>Search string: ( 5216302 or 6075395 or 5304953 or 5355037 or 4719608 or 5179303 or 4999526 or 5223755 or 6229363 or 5204564 or 4859954 or 5376849 ).pn</p>							
<b>US Patent Documents</b>							
<b>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</b>							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
CE	1	5216302	1993-06-01	Tanizawa		307	603
CE	2	6075395	2000-06-13	Saeki		327	161
CE	3	5304953	1994-04-19	Heim et al.		331	1A
CE	4	5355037	1994-10-11	Andresen et al.		307	602
CE	5	4719608	1988-01-12	Genat et al.		368	113
CE	6	5179303	1993-01-12	Searles et al.		307	603
CE	7	4999526	1991-03-12	Dudley		307	269
CE	8	5223755	1993-06-29	Richley		307	603
CE	9	6229363	2001-05-08	Eto et al.	B1	327	158
CE	10	5204564	1993-04-20	Ochiai		307	603
CE	11	4859954	1989-08-22	Yoshimura		328	155
CE	12	5376849	1994-12-27	Dickol et al.		327	278
<b>Signature</b>							
Examiner Name				Date			
Cassandra Cox				October 30, 2005			